

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Blaine D. Gaither, et al.

Confirmation No.: 3480

Application No.: 10/002,971

Examiner: Henry Tsai

Filing Date: Oct. 26, 2001

Group Art Unit: 2183

Title: Method Of Optimization Of CPU And Chipset Performance By Support Of Optional Reads By CPU And Chipset

Mail Stop AF
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment () Petition to extend time to respond
() New fee as calculated below () Supplemental Declaration
(X) No additional fee
(X) Other: Return Post Card (fee \$)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	20	MINUS	20	= 0	X \$50	\$ 0
INDEP. CLAIMS	3	MINUS	3	= 0	X \$200	\$ 0
[] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$360	\$ 0
EXTENSION FEE	1ST MONTH \$120.00	2ND MONTH \$450.00	3RD MONTH \$1020.00	4TH MONTH \$1590.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

"Express Mail" label no. EV413243259US

Date of Deposit Feb. 13, 2006

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By Melissa Smith

Typed Name: Melissa Smith

Respectfully submitted,

Blaine D. Gaither, et al.

By

Curtis A. Vock

Attorney/Agent for Applicant(s)
Reg. No. 38,356

Date: Feb. 13, 2006

Telephone No.: (720) 931-3011



PATENT
Attorney Docket No. 10018224-1
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	Blaine D. Gaither, et al.	Examiner	Henry Tsai
Serial No.	10/002,971	Group Art No.	2181
Filed	October 26, 2001	Confirmation No.	3480
For	Method Of Optimization Of CPU And Chipset Performance By Support Of Optional Reads By CPU And Chipset		

February 13, 2006

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Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

Dear Sir:

Responsive to the Final Office Action mailed December 13, 2005, please
amend the above-titled application (hereinafter the '971 Application) as follows:

IN THE CLAIMS begins on page 2 of this paper.

REMARKS begin on page 5 of this paper.